Hex inverting Schmitt trigger Rev. 3 — 8 September 2023

### 1. General description

The 74AHCT14A is a hex inverter with Schmitt-trigger inputs, capable of transforming slowly changing input signals into sharply defined, jitter-free output signals.

Designed to operate over a V<sub>CC</sub> range from 4.5 V to 5.5 V, the inputs are TTL compatible. This range allows the device to be used to translate from 3.3 V to 5 V.

This device is fully specified for partial Power-down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

### 2. Features and benefits

- Direct interface with TTL levels
- Supply voltage range from 4.5 V to 5.5 V
- Typical t<sub>pd</sub> of 3.7 ns at 5 V
- Typical V<sub>OL(p)</sub> < 0.8 V at V<sub>CC</sub> = 5 V, T<sub>amb</sub> = 25 °C
- Typical  $V_{OH(v)}$  > 2.3 V at  $V_{CC}$  = 5 V,  $T_{amb}$  = 25 °C
- Supports mixed-mode voltage operation on all ports
- I<sub>OFF</sub> circuitry provides partial Power-down mode operation
- Latch-up performance exceeds 250 mA per JESD 78 Class II
- ESD protection:
  - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 3000 V
  - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 2000 V
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

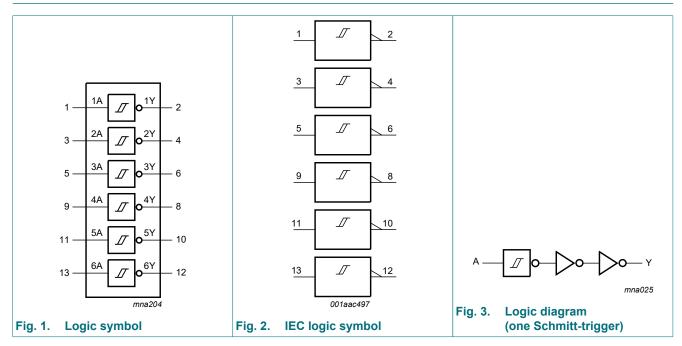
### 3. Ordering information

Table 1. Ordering information							
Type number	er Package						
	Temperature range	Name	Description	Version			
74AHCT14APW	-40 °C to +125 °C	TSSOP14	plastic thin shrink small outline package; 14 leads; body width 4.4 mm	<u>SOT402-1</u>			



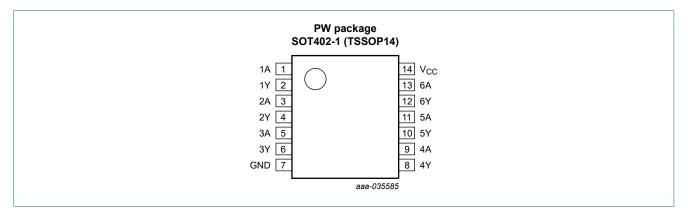
#### Hex inverting Schmitt trigger

# 4. Functional diagram



### 5. Pinning information

### 5.1. Pinning



### 5.2. Pin description

Table 2. Pin description						
Symbol	Pin	Description				
1A, 2A, 3A, 4A, 5A, 6A	1, 3, 5, 9, 11, 13	data input				
1Y, 2Y, 3Y, 4Y, 5Y, 6Y	2, 4, 6, 8, 10, 12	data output				
GND	7	ground (0 V)				
V <sub>CC</sub>	14	supply voltage				

74AHCT14A

### 6. Functional description

#### Table 3. Function table

H = HIGH voltage level; L = LOW voltage level.

Input	Output
nA	nY
L	Н
Н	L

### 7. Limiting values

#### Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Мах	Unit
V <sub>CC</sub>	supply voltage			-0.5	+7.0	V
VI	input voltage		[1]	-0.5	+7.0	V
Vo	output voltage	output HIGH or LOW state [2]	[3]	-0.5	V <sub>CC</sub> + 0.5	V
		output power-down	[2]	-0.5	+7.0	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < 0 V		-20	-	mA
I <sub>OK</sub>	output clamping current	V <sub>O</sub> < 0 V		-20	-	mA
I <sub>O</sub>	output current	$V_{O} = 0 V \text{ to } V_{CC}$		-	±25	mA
I <sub>CC</sub>	supply current			-	75	mA
I <sub>GND</sub>	ground current			-75	-	mA
T <sub>stg</sub>	storage temperature			-65	+150	°C
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = -40 °C to +125 °C	[4]	-	500	mW

[1] If the input current ratings are observed, the minimum input voltage ratings may be exceeded.

[2] If the output current ratings are observed, the output voltage ratings may be exceeded.

[3] This value is limited to 7 V maximum.

[4] For SOT402-1 (TSSOP14) package: P<sub>tot</sub> derates linearly with 7.3 mW/K above 81 °C.

### 8. Recommended operating conditions

#### Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CC</sub>	supply voltage		4.5	5.0	5.5	V
VI	input voltage		0	-	5.5	V
Vo	output voltage	output HIGH or LOW state	0	-	V <sub>CC</sub>	V
		output power-down	0	-	5.5	V
T <sub>amb</sub>	ambient temperature		-40	+25	+125	°C
Δt/ΔV	input transition rise and fall rate	V <sub>CC</sub> = 5.0 V ± 0.5 V	-	-	1	ms/V

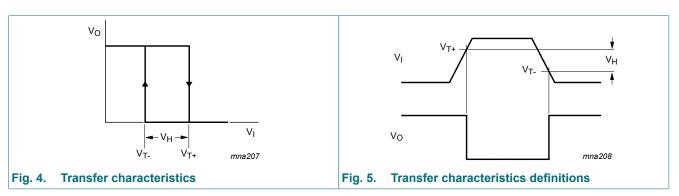
# 9. Static characteristics

#### **Table 6. Static characteristics**

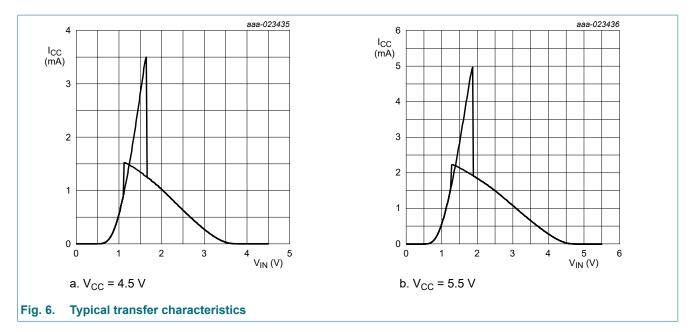
Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	
V <sub>T+</sub>	positive-going	V <sub>CC</sub> = 4.5 V	-	-	1.9	-	1.9	-	1.9	V
	threshold voltage	V <sub>CC</sub> = 5.5 V	-	-	2.1	-	2.1	-	2.1	V
V <sub>T-</sub>	negative-going	V <sub>CC</sub> = 4.5 V	0.5	-	-	0.5	-	0.5	-	V
	threshold voltage	V <sub>CC</sub> = 5.5 V	0.6	-	-	0.6	-	0.6	-	V
V <sub>H</sub>	hysteresis	V <sub>CC</sub> = 4.5 V	0.4	-	1.4	0.4	1.4	0.4	1.4	V
	voltage	V <sub>CC</sub> = 5.5 V	0.4	-	1.5	0.4	1.5	0.4	1.5	V
V <sub>OH</sub>	HIGH-level	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I <sub>O</sub> = -50 μA	4.4	4.5	-	4.4	-	4.4	-	V
	I <sub>O</sub> = -8 mA	3.94	-	-	3.8	-	3.7	-	V	
V <sub>OL</sub>	LOW-level	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I <sub>O</sub> = 50 μA	-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 8 mA	-	-	0.36	-	0.44	-	0.55	V
I <sub>OFF</sub>	power-off leakage current	$V_1 \text{ or } V_0 = \text{GND to 5.5 V};$ $V_{CC} = 0 \text{ V}$	-	-	0.5	-	5	-	5	μA
l <sub>l</sub>	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 0 V$ to 5.5 V	-	-	±0.1	-	±1	-	±1	μA
I <sub>CC</sub>	supply current	V <sub>I</sub> = V <sub>CC</sub> or GND; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 5.5 V	-	-	2	-	20	-	20	μA
ΔI <sub>CC</sub>	additional supply current	per input pin; V <sub>I</sub> = 3.4 V; I <sub>O</sub> = 0 A; other pins at V <sub>CC</sub> or GND; V <sub>CC</sub> = 5.5 V	-	-	1.35	-	1.5	-	1.5	mA

### 9.1. Transfer characteristics waveforms



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# 10. Dynamic characteristics

#### Table 7. Dynamic characteristics

GND = 0 V. For test circuit, see Fig. 8.

Symbol Parameter		Conditions	25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit	
			Min	Typ[1]	Max	Min	Max	Min	Max	
t <sub>pd</sub>	propagation	nA to nY; see Fig. 7 [2]								
	delay	V <sub>CC</sub> = 4.5 V to 5.5 V								
	C <sub>L</sub> = 15 pF	-	3.7	7.6	1	9	1	9.7	ns	
		C <sub>L</sub> = 50 pF	-	5	9.6	1	11	1	11.9	ns
CI	input capacitance	$V_I = V_{CC}$ or GND; $V_{CC} = 5 V$	-	2	6	-	6	-	6	pF
Co	output capacitance	$V_{O} = V_{CC}$ or GND; $V_{CC} = 5 V$	-	5	-	-	-	-	-	pF
C <sub>PD</sub>	power dissipation capacitance	per buffer; $C_L = 0 \text{ pF}$ ; [3] f = 10 MHz; $V_I = \text{GND to } V_{CC}$	-	10	-	-	-	-	-	pF

Typical values are measured at  $T_{amb}$  = 25 °C and  $V_{CC}$  = 5 V. [1]

[2]  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ . [3]  $C_{PD}$  is used to determine the dynamic power dissipation  $P_D (\mu W)$ .  $P_D = C_{PD} \times V_{CC}^2 \times f_i + \sum (C_L \times V_{CC}^2 \times f_o)$  where:  $f_i =$  input frequency in MHz;

f<sub>o</sub> = output frequency in MHz;

 $C_L$  = output load capacitance in pF;

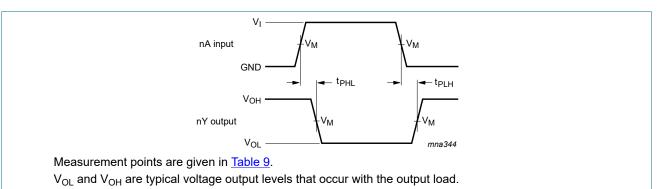
 $V_{CC}$  = supply voltage in V.

### Table 8. Noise characteristics

GND = 0 V. For test circuit, see Fig. 8.

Symbol	Parameter	Conditions	Т	T <sub>amb</sub> = 25 °C			
			Min	Тур	Max		
V <sub>CC</sub> = 5 V	/; C <sub>L</sub> = 50 pF						
V <sub>OL(p)</sub>	LOW-level output voltage (peak)		-	0.4	0.8	V	
V <sub>OL(v)</sub>	LOW-level output voltage (valley)		-0.8	-0.2	-	V	
V <sub>OH(v)</sub>	HIGH-level output voltage (valley)		-	4.5	-	V	
V <sub>IH(AC)</sub>	AC HIGH-level input voltage		2	-	-	V	
V <sub>IL(AC)</sub>	AC LOW-level input voltage		-	-	0.8	V	

### 10.1. Waveforms and test circuit

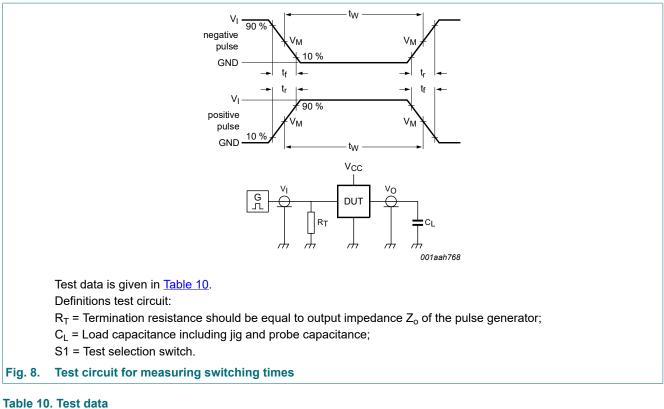


### Fig. 7. Propagation delay input (nA) to output (nY)

#### Table 9. Measurement points

Input	Output
V <sub>M</sub>	V <sub>M</sub>
1.5 V	$0.5 \times V_{CC}$

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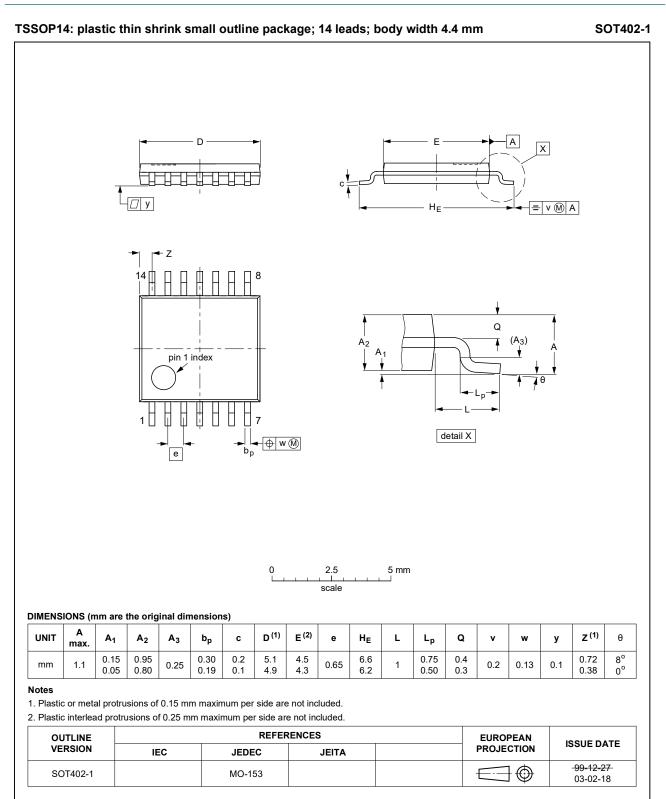
Input		Load	Test
VI	t <sub>r</sub> , t <sub>f</sub>	CL	
GND to 3 V	3.0 ns	15 pF, 50 pF	t <sub>PLH</sub> , t <sub>PHL</sub>

**Product data sheet** 

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# 11. Package outline



#### Fig. 9. Package outline SOT402-1 (TSSOP14)

# **12. Abbreviations**

Acronym	Description
CDM	Charge Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
TTL	Transistor-Transistor Logic

# 13. Revision history

### Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes			
74AHCT14A v.3	20230908	Product data sheet	-	74AHCT14A v.2			
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li><u>Section 2</u>: ESD specification updated according to the latest JEDEC standard.</li> <li><u>Table 4</u>: Derating values for P<sub>tot</sub> total power dissipation updated.</li> </ul>						
74AHCT14A v.2	20161026	Product data sheet	-	74AHCT14A v.1			
Modifications:	Type numbers 74AHCT14AD and 74AHCT14ABQ removed.						
74AHCT14A v.1	20160614	Product data sheet					

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# 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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